☆☆☆ <u>Updating of FBs OS V4.47</u> 11/21/2006

- Remove the bug The current value of hardware high speed counter will be reset to zero while the main unit stays at STOP mode and turns the power off
- Remove the bug When the hardware high speed timer (HST0~3) has been configured for working, too early starts the timer, there may cause the undesired interrupt processing
- Modify the communication processing to enhance the intellectual protection for both password & ID methodology