Chapter 4 FB-PLC Instruction Lists

4.1 Sequential Instructions

Instruction	Operand	Symbol	Function Descriptions	Execution Time	Instruction type
ORG		+	Starting a network with a normally open (A) contact		
ORG NOT	X,Y,M,	↓ /	Starting a network with a normally closed (B) contact	0.33uS	
ORG TU	S,T,C	├ ↑	Starting a network with a differential up (TU) contact	0.54.0	Network
ORG TD		┝┤↓├─	Starting a network with a differential down (TD) contact	0.54uS	starting
ORG OPEN		• • •	Starting a network with a open circuit contact	0.00.0	
ORG SHORT		+	Starting a network with a short circuit contact	0.3305	
LD		$\neg \vdash$	Starting a relay circuit from origin or branch line with a normally open contact	0.0000	
LD NOT	X,Y,M,		Starting a relay circuit from origin or branch line with a normally closed contact	0.3305	
LD TU	S,T,C		Starting a relay circuit from origin or branch line with a differential up contact	0.54.0	Origin or branch line
LD TD			Starting a relay circuit from origin or branch line with a differential down contact	0.54uS	starting
LD OPEN		o o	Starting a relay circuit from origin or branch line with a open circuit contact		Instructions
LD SHORT		••	Starting a relay circuit from origin or branch line with a short circuit contact	0.33uS	
AND			Serial connection of normally open contact		
AND NOT	X,Y,M,		Serial connection of normally closed contact	0.33uS	
AND TU	S,T,C		Serial connection of differential up contact		Serial
AND TD			Serial connection of differential down contact	0.54uS	connection instructions
AND OPEN		o o	Serial connection of open circuit contact	0.0000	
AND SHORT		·•	Serial connection of short circuit contact	0.3305	
OR			Parallel connection of normally open contact		
OR NOT	X,Y,M,		Parallel connection of normally closed contact	0.33uS	
OR TU	S,T,C		Parallel connection of differential up contact	0.54.0	Parallel
OR TD		⁺⊣↓⊢⁺	Parallel connection of differential down contact	0.54uS	connection instructions
OR OPEN		⁺_ ₀ _ †	Parallel connection of open circuit contact	0.00.0	
OR SHORT		<u>+</u> +	Parallel connection of short circuit contact	0.3308	
ANDLD			Serial connection of two circuit blocks	0.00.0	Blocks merge
ORLD			Parallel connection of two circuit blocks	0.3308	instructions

Instruction	Operand	Symbol	Function Descriptions	Execution Time	Instruction type
OUT	VMC	()	Send result to coil		
OUT NOT	¥,IVI,S	(<i>/</i>)	Send inverted result to coil	0.33uS	Coil output
OUT L	Y	(L)	Send result to an external output coil and appoint it as of retentive type	1.09uS	instruction
OUT	TD		Save the node status to a temporary relay	0.0000	
LD	IK		Load the temporary relay	0.3305	
ти		— ↑—	Take the transition up of the node status	0.33uS	Node operation instruction
тр		↓	Take the transition down of the node status	0.33uS	
NOT		—/—	Invert the node status	0.33uS	

• The 34 sequential instructions listed above are all applicable to every models of FB-PLC.

4.2 Function Instructions

There are more than 100 different FB-PLC function instructions. If put the "D" and "P" derivative instructions into account, the total number of instructions is over 300. On top of these, many function instructions have multiple input controls (up to 4 inputs) which can have up to 8 different types of operation mode combinations. Hence, the size of FB-PLC instruction sets is in fact not smaller than that of a large PLC. Having powerful instruction functions, though may help for establishing the complicated control applications, but also may impose a heavy burden on those users of small type PLC's. For ease of use, FATEK PLC function instructions are divided into two groups, the Basic function group which includes 26 commonly used function instructions and 4 SFC instructions and the advanced function group which includes other more complicated function instructions, such as high-speed counters and interrupts. This will enable the beginners and the non-experienced users to get familiar with the basic function very quickly and to assist experienced users in finding what they need in the advanced set of function instructions.

The instructions attached with " \star " symbol are basic functions which amounts to 26 function instructions and 4 SFC instructions. All the basic functions will be explained in next chapter. The details for the reset of functions please refer advanced manual.

■ General Timer/Counter Function Instructions

FUN No.	Name	Operand	Derivative Instruction	Function descriptions
*	T nnn	PV		General timer instructions ("nnn" range $0 \sim 255$)
*	C nnn	PV		General counter instructions ("nnn" range $0 \sim 255$)

■ Single Operand Function Instructions

★ 4	DIFU	D	To get the up differentiation of a D relay and store the result to D
★ 5	DIFD	D	To get the down differentiation of a D relay and store the result to D
★ 10	TOGG	D	Toggle the status of the D relay

SET/RESET Instructions

FUN No.	Name	Operand	Derivative instruction	Function descriptions
*	SET	D	DP	Set a discrete or all bits of a register to 1
*	RST	D	DP	Reset a discrete or all bits of a register to 0

SFC Instructions

*	STP	Snnn	STEP declaration
*	STPEND		End of the STEP program
*	то	Snnn	STEP divergent instruction
*	FROM	Snnn	STEP convergent instruction

Arithmetical Operation Instructions

★ 11	(+)	Sa,Sb,D	DP	Perform addition of Sa and Sb and then store the result to D
★ 12	(-)	Sa,Sb,D	DP	Perform subtraction of Sa and Sb and then store the result to D
★ 13	(*)	Sa,Sb,D	DP	Perform multiplication of Sa and Sb and then store the result to D
★ 14	(⁄)	Sa,Sb,D	DP	Perform division of Sa and Sb and then store the result to D
15	(+1)	D	DP	Adds 1 to the D value
16	(-1)	D	DP	Subtracts 1 from the D value
23	DIV48	Sa,Sb,D	Р	Perform 48 bits division of Sa and Sb and then store the result to D
24	SUM	S,N,D	DP	Take the sum of the successive N values beginning from S and store it in D
25	MEAN	S,N,D	DP	Take the mean average of the successive N values beginning from S and store it in D
26	SQRT	S,D	DP	Take the square root of the S value and store it in D
27	NEG	D	DP	Take the 2's complement (negative number) of the D value and store it back in D
28	ABS	D	DP	Take the absolute value of D and store it back in D
29	EXT	D	Р	Take the 16 bit numerical value and extend it to 1 32 bit numerical value (value will not change)
30	PID	TS,SR,OR, PR,WR		PID operation

Logical Operation Instructions

★ 18	AND	Sa,Sb,D	DP	Perform logical AND for Sa and Sb and store the result to D
★ 19	OR	Sa,Sb,D	DP	Perform logical OR for Sa and Sb and store the result to D
35	XOR	Sa,Sb,D	DP	Take the result of the Exclusive OR logical operation made between Sa and Sb, and store it in D
36	XNR	Sa,Sb,D	DP	Take the result of the Exclusive OR logical operation made between Sa and Sb, and store it in D

Comparison Instructions

★ 17	CMP	Sa,Sb	DP	Compare the data at Sa and data at Sb and output the result to function outputs (FO)
37	ZNCMP	S,Su,SL	DP	Compare S with the zones formed by the upper limit Su and lower limit SL, and set the result to FO0~FO2

Data Movement Instructions

FUN No.	Name	Operand	Derivative instruction	Function descriptions
★ 8	MOV	S,D	DP	Transfer the W or DW data specified at S to D
★9	MOV/	S,D	DP	Invert the W or DW data specified at S, and then transfers the result to D
40	BITRD	S,N	DP	Read the status of the bits specified by N within S, and send it to FO0
41	BITWR	D,N	DP	Write the INB input status into the bits specified by N within D
42	BITMV	S,Ns,D,Nd	DP	Write the status of bit specified by N within S into the bit specified by N within D
43	NBMV	S,Ns,D,Nd	DP	Write the Ns nibble within S to the Nd nibble within D
44	BYMV	S,Ns,D,Nd	DP	Write the byte specified by Ns within S to the byte specified by Nd within D
45	XCHG	Da,Db	DP	Exchange the values of Da and Db
46	SWAP	D	Р	Swap the high-byte and low-byte of D
47	UNIT	S,N,D	Р	Take the nibble 0 (NB0) of the successive N words starting from S and combine the nibbles sequentially then store in D
48	DIST	S,N,D	Р	De-compose the word into successive N nibbles starting from nibble 0 of S, and store them in the NB0 of the successive N words starting from D

Shifting/Rotating Instructions

★6	BSHF	D	DP	Shift left or right 1 bit of D register
51	SHFL	D,N	DP	Shift left the D register N bits and move the last shifted out bits to OTB. The empty bits will be replaced by INB input bit
52	SHFR	D,N	DP	Shift right the D register N bits and move the last shifted out bits to OTB, The empty bits will be replaced by INB input bit
53	ROTL	D,N	DP	Rotate left the D operand N bits and move the last rotated out bits to OTB
54	ROTR	D,N	DP	Rotate right the D operand N bits and move the last rotated out bits to OTB

Code Conversion Instruction

★ 20	→BCD	S,D	DP	Convert binary data of S into BCD data and store the result to D
★ 21	→BIN	S,D	DP	Convert BCD data of S into binary data and store the result to D
57	DECOD	S,Ns,NL,D	Р	Decode the binary data formed by N_L bits starting from Ns bit within S, and store the result in the register starting from D
58	ENCOD	S,Ns,NL,D	Р	Encoding the NL bits starting from the Ns bit within S, and store the result in D
59	→7SG	S,N,D	Р	Convert the N+1 number of nibble data within S, into 7 segment code, then store in D

FUN No.	Name	Operand	Derivative instruction	Function descriptions
60	→ASC	S,D	Р	Write the constant string S (max. 12 alpha-numeric or symbols) into the registers starting from D
61	→SEC	S,D	Р	Convert the time data (hours, minutes, seconds) of the three successive registers starting from S into seconds data then store to D
62	→HMS	S,D	Ρ	Convert the seconds data of S into time data (hours, minutes, seconds) and store the data in the three successive registers starting from D
63	→HEX	S,N,D	Р	Convert the successive N ASCII data starting from S into hexadecimal data and store them to D
64	→ASCⅡ	S,N,D	Р	Convert the successive N hexadecimal data starting from S into ASCII codes and store them to D

Flow Control Instructions

★0	MC	N		The start of master control loop
★1	MCE	Ν		The end of master control loop
★2	SKP	Ν		The start of skip loop
★ 3	SKPE	N		The end of skip loop
	END			End of Program
65	LBL	1∼6 alphanumeric		Define the label with 1~6 alphanumeric string
66	JMP	LBL	Р	Jump to LBL label and continues the program execution
67	CALL	LBL	Р	Call the sub-program begin with LBL label
68	RTS			Return to the calling main program from sub-program
69	RTI			Return to interrupted main program from sub-program
70	FOR	N		Define the starting point of the FOR Loop and the loop count N
71	NEXT			Define the end of FOR loop

Temperature Control Function Instructions

72	TP4	Tp,Pe,Sm,Ym, AR,TR,WR	Temperature module FB-2AJ(K,H,T)4 convenient instruction for measurement
73	TSTC	Tp,Pe,Sm,Ym, AR,TR,Yn,Sn, Zh,Sv,Os,PR, IR,DR,OR,WR	Temperature module FB-2AJ(K,H,T)4 convenient instruction for measurement and PID control
85	TPSNS	Tp,PI,Zn, Yn,SR,WR	Temperature module FB-4AJ(K)XX convenient instruction for measurement
86	TPCTL	Yn,Sn,Zn, Sv,Os,PR IR,DR,OR,WR	Temperature module FB-4AJ(K)XX convenient instruction for PID control

I/O Function Instructions

FUN No.	Name	Operand	Derivative instruction	Function descriptions
74	IMDIO	D,N	Р	Update the I/O signal on the main unit immediately
75	FILT	N	Р	Set the input filter time of high speed input X0 \sim X15 to N mS
76	TKEY	IN,D,KL	D	Convenient instruction for 10 numeric keys input
77	HKEY	IN,OT,D,KL	D	Convenient instruction for 16 keys input
78	DSW	IN,OT,D	D	Convenient instruction for digital switch input
79	7SGDL	S,OT,N	D	Convenient instruction for multiplexing 7-segment display
80	MUXI	IN,OT,N,D		Convenient instruction for multiplexing input instruction
81	PLSO	MD, Fr, PC UY,DY,HO	D	Pulse output function (for bi-directional drive of step motor)
82	PWM	TO,TP,OT		Pulse width modulation output function
83	SPD	S,TI,D		Speed detection function
84	7SGMO	S,Yn,Dn, PT,IT,WS		Convenient instruction for 7-segment display module(FB-7SGXX)

Cumulative Timer Function Instructions

87	T.01S	CV,PV	Cumulative timer using 0.01S as the time base
88	T.1S	CV,PV	Cumulative timer using 0.1S as the time base
89	T1S	CV,PV	Cumulative timer using 1S as the time base

■ Watch Dog Timer Control Function Instructions

90	WDT	Ν	Р	Set the WDT timer time out time to N mS
91	RSWDT		Р	Reset the WDT timer to 0

High Speed Counter Control Function Instructions

92	HSCTR	CN	Р	Read the current CV value of the hardware HSCs, HSC0~HSC3, or HST on ASIC to the corresponding CV register in the PLC respectively
93	HSCTW	CN,D	Р	Write the CV or PV register of HSC0 \sim HSC3 or HST in the PLC to CV or PV register of the hardware HSC or HST on ASIC respectively

Report Function Instructions

			Parse and generate the report message b	sed on the ASCII
94	4 ASCWR MD,S,Pt	formatted data starting from the address S. The	report message will	
			send to port1	

Ramp Function Instructions

95	RAMP	Tn,PV,SL, SU,D		Ascending/Descending convenient instruction
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Communication Function Instructions

96	LINK2	MD,S,Pt	Convenient instruction for port2 communication control
97	LINK1	MD,S,Pt	Convenient instruction for port1 communication control

Table Function Instructions

FUN No.	Name	Operand	Derivative instruction	Function descriptions
100	R→T	Rs,Td,L,Pr	DP	Store the Rs value into the location pointed by the Pr in Td
101	T→R	Ts,L,Pr,Rd	DP	Store the value at the location pointed by the Pr in Ts into Rd
102	T→T	Ts,Td,L,Pr	DP	Store the value at the location pointed by the Pr in Ts into the location pointed by the Pr in Td
103	BT_M	Ts,Td,L	DP	Copy the entire contents of Ts to Td
104	T_SWP	Ta,Tb,L	DP	Swap the entire contents of Ta and Tb
105	R-T_S	Rs,Ts,L,Pr	DP	Search the table Ts to find the location with data different or equal to the value of Rs. If found store the position value into the Pr
106	T-T_C	Ta,Tb,L,Pr	DP	Compare two tables Ta and Tb to search the entry with different or same value. If found store the position value into the Pr
107	T_FIL	Rs,Td,L	DP	Fill the table Td with Rs
108	T_SHF	IW,Ts,Td, L,OW	DP	Store the result into Td after shift left or right one entry of table Ts. The shift out data is send to OW and the shift in data is from IW
109	T_ROT	Ts,Td,L	DP	Store the result into Td after shift left or right one entry of table Ts.
110	QUEUE	IW,QU,L, Pr,OW	DP	Push IW into QUEUE or get the data from the QUEUE to OW (FIFO)
111	STACK	IW,ST,L, Pr,OW	DP	Push IW into STACK or get the data from the STACK to OW (LIFO)
112	BKCMP	Rs,Ts,L,D	DP	Compare the Rs value with the upper/lower limits of L, constructed by the table Ts, then store the comparison result of each pair into the relay designated by D (DRUM)
113	SORT	S,D,L	DP	Sorting the registers starting from S length L and store the sorted result to D

Matrix Instructions

120	MAND	Ma,Mb,Md,L	Р	Store the results of logic AND operation of Ma and Mb into Md
121	MOR	Ma,Mb,Md,L	Р	Store the results of logic OR operation of Ma and Mb into Md
122	MXOR	Ma,Mb,Md,L	Р	Store the results of logic Exclusive OR operation of Ma and Mb into Md
123	MXNR	Ma,Mb,Md,L	Р	Store the results of logic Exclusive OR operation of Ma and Mb into Md
124	MINV	Ms,Md ,L	Р	Store the results of inverse Ms into Md
125	MCMP	Ma,Mb,L Pr	Р	Compare Ma and Mb to find the location with different value, then store the location into Pr
126	MBRD	Ms,L,Pr	Р	Read the bit status pointed by the Pr in Ms to the OTB output
127	MBWR	Md,L,Pr	Р	Write the INB input status to the bits pointed by the Pr in Ms
128	MBSHF	Ms,Md,L	Р	Store the results to Md after shift one bit of the Ms. Shifted out bit will appear at OTB and the shift in bits comes from INB
129	MBROT	Ms,Md,L	Р	Store the results to Md after rotate one bit of the Ms. Rotated out bit will appear at OTB.
130	MBCNT	Ms,L,D	Р	Calculate the total number of bits that are 0 or 1 in Ms, then store the results into D

NC Positioning Instruction

FUN No.	Name	Operand	Derivative instruction	Function descriptions
140	HSPSO	Ps,SR,WR		HSPSO instruction of NC positioning control
141	MPARA	Ps,SR	Р	Parameter setting instruction of NC positioning control
142	PSOFF	Ps	Р	Stop the pulse output of NC positioning control
143	PSCNV	Ps,D	Р	Convert the Ps positions of NC positioning to mm, Inch or Deg

Disable/Enable Control of Interrupt or Peripheral

145	EN	LBL	Р	Enable HSC, HST, external INT or peripheral operation
146	DIS	LBL	Р	Disable HSC, HST, external INT or peripheral operation