☆☆☆ Updating of FBs OS V4.49 03/22/2007

● Supports FBs-4A2D on-board analog expansion; the interfaces are shown as below :

. D4071 : This register tells the installation information

High Byte = 5AH, main unit equips intelligent board

= Other values, main unit without intelligent board

Low Byte = 2, on-board is FBs-B2A1D

= 3, on-board is FBs-B2DA

= 4, on-board is FBs-B4AD

= 15, on-board is FBs-B4A2D (Custom-made)

.Port 1 : This port is used as the communication interface between main unit and on-board expansion; communication settings are fixed as below:

. FATEK communication protocol

. Communication parameters

.Baud Rate : 104727 bps

.Data Bit : 7-bit

.Parity : Even

.Stop Bit : 1-bit

.M1960=0, Port 1 is busy

.R4040 High Byte=0, Non delay for reply

Name	Value of D4071	Analog Input	Analog Output	Specification
FBs-B2A1D	5A02H	2 channels D4072 : AI_0 D4073 : AI_1	1 channel D4076: AO_0	.Resolution : 12-bit .Data Format : 14-bit, but valid12-bit (0～16380) .Type of Signal : Voltage: 0～10V or Current: 0～20mA .Without isolation between channels
FBs-B2DA	5A03H	None	2 channels D4076: AO_0 D4077: AO_1	
FBs-B4AD	5A04H	4 channels D4072 : AI_0 D4073 : AI_1 D4074 : AI_2 D4075 : AI_3	None	
FBs-B4A2D	5A0FH	4 channels D4072 : AI_0 D4073 : AI_1 D4074 : AI_2 D4075 : AI_3	2 channels D4076: AO_0 D4077: AO_1	

.Data format of 14-bit, but valid 12-bit representation (0～16380) :

b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
0	0	0/1	0/1	0/1	0/1	0/1	0/1	0/1	0/1	0/1	0/1	0/1	0/1	0	0

● Modify the system OS firmware to match the new power supply board (Power failure detection by detecting the AC power source)