## 1.4 Main Unit Specifications

Items

Execution Speed

"\*" is default, user configurable Specification20K Words Program + 3K Words ROR + 8K Words Comment

D. C.						
Program Capacity					20K Words Program + 3K Words ROR + 8K Words Comment	
Program Memory					ROM Pack or RAM+ Lithium Battery Back-up	
Sequential Instruction					34 Instructions	
MA Model					275 Instructions (103 Kinds)	Include derivative
Function Instructions MC Model				MC Model	300 Instructions (109 Kinds)	instruction
Sequential Flow Command (SFC)				(SFC)	4	
Discrete (Bit	X	Digital Input (DI)			X0~X255 (256)	
	Y	Digital Output (DO)			Y0~Y255 (256)	
	TR	Temporary Relay			$TR0 \sim TR39 (40)$	
	М	Internal Relay		Non-retentive	M0~M799 (800)* Note: Can be configured as Retentive M1400~M1911 (512)	
				Retentive	M800~M1399 (600)* Note: Can be configured as Non-retentive	
		Special Re	elay		M1912~M2001 (90)	
Status)	S	Step Relay		Non-retentive	$S0 \sim S499$ (500)* Note: $S20 \sim S499$ can be configured as Retentive	
				Retentive	S500~S999 (500)* Note: Can be configured as Non-retentive	
	Т	Timer "Time Up" Status Contact			T0~T255 (256)	
	С	Counter "Count Up" Status Contact			C0~C255 (256)	
	TMR	Timer 0.01		S Time Base	T0~T49 (50)*	The quantity of each time base can
		Current Value Register	0.18	Time Base	T50~T199 (150)*	be configured
			1S T	ime Base	T200~T255 (56)*	
	CTR	Counter Current Value Register	16 Bits	Retentive	C0~C139 (140)* Note: Can be configured as Non-retentive	
				Non-retentive	Non-retentive C140~C199 (60)* Note: Can be configured as Retentive	
Registe			32 Bits	Retentive	C200~C239 (40)* Note: Can be configured as Non-retentive	
r « W				Non-retentive	C240~C255 (16)* Note: Can be configured as Retentive	
Register 《Word Ddata》	HR DR	Data Register		Retentive	$R0 \sim R2999 (3000)^*$ Note: Can be configured as Non-retentive $D0 \sim D3999 (4000)$	
				Non-retentive	R3000~R3839 (840)* Note: Can be configured as Retentive	
	HR			Retentive	R5000~R8071 (3072)* Note: While general purpose register, It allows read/write operation	
	ROR FR			Read Only Register	R5000~R8071 (0)* Note: Can be configured as ROR	ROR memory is out of program memory
				File register	F0~F8191 (8192) Note: Need dedicated instruction to access	

0.33uS/Sequential Instruction

	IR	Input Register			R3840~R3903 (64)				Analog Input or Register Input
	OR	Output Register			R3904~R3967 (64)				Analog Output or Register Output
	SR	System Special Register			R3968~R4167 (200) · D4000~D4095 (96)				
		0.1mS High-Speed Timer Register			R4152~R4154 (3)				
		High-Spee	d Ha	rdware (4 ets)	DR4096~DR4110 (4×4)				
	Specia	Counter Register	So	ftware (4 sets)	DR4112~DR4126 (4×4)				
	Special Register	register	l		R4128	R4129	R4130	R4131	
	ter	Calendar Register		(Second)	(Minute)	(Hour)	(Date)		
				R4132 (Month)	R4133 (Year)	R4134 (Week)			
	XR	Index Register				P0~P9 (10)	()		
	nterrupt	External Input Interrupt			32 interrupts (16 points input of positive/negative edge)				
	Control	Internal Fixed Time Interrupt			8 modes (1 · 2 · 3 · 4 · 5 · 10 · 50 · 100mS)				
0.1	mS High Sp	eed Timer			1 (16-bit) · 4 (	(32-bit, share with	HHSC)		
		*** 1 0 1	Quantity		Max. 4 sets			Total of HHSC and SHSC are 8 sets HHSC can be defined as 32-bit/0.1mS time base high speed	
Hig	Counter (I	High-Speed HHSC)	Counting mode		8 modes $(U/D \cdot U/D \times 2 \cdot K/R \cdot K/R \times 2 \cdot A/B \cdot A/B \times 2 \cdot A/B \times 3 \cdot A/B \times 4)$				
High Speed Counter	/32-bit		Counting frequency		Maximum 100KHz (Single-ended) or 750KHz (Differential)				
			Quantity		4 for MC model; 2 for MA model				
	Software Counter (	High-Speed SHSC)	Counti	ng mode	3 modes (U/D · K/R · A/B)		timer		
	/32-bit		Counting frequency		Total frequency up to 10KHz				
	Port 0 (RS-232 or USB)				Baud Rate: 4.8Kbps~921.6Kbps				Default is 9.6Kbps
C					Baud Rate :4.8Kbps ~ 921.6Kbps *				
Communications	Port 1∼	4 (RS-232 o	r RS-48	5)	Note: Supporting FATEK/Modbus RTU communication protocol				
				& built-in CPU Link ability					
	Maximum Link Stations				254				
Hi	gh-Speed				Max. 4 Axes				
Pu	lse Output	Maximum Output Frequency			100KHz (Single-ended) \ 750KHz (Differential)				
	for NC	Pulse Output Mode			3 modes (U/D \cdot K/R \cdot A/B)				
Po	ositioning	Position Language			Text mode Table Editing Language				
		Number o	Points		Max. 4 Points				
	gh-Speed WM				72Hz~18.432KHz (Resolution is 0.1%)				
	Output	Output Free	quency		720Hz~184.32KHz (Resolution is 1%)				
Caj	Captured Input Max. 36 P				Points				•
Dig	X0~X15: Digital Filter for Digital Input				Filtering by 7	Γime or by Free		: (1~15)×0.1mS uency: 14K~1.8M	
				X16~X35	Filtering by Time $(1\sim15)\times1$ mS				