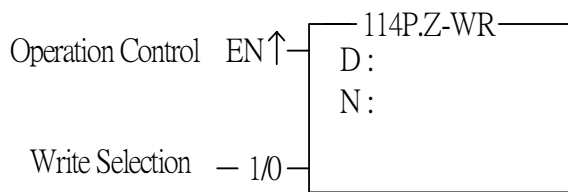


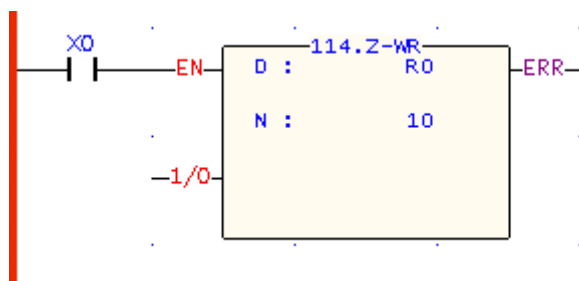
FUN114 Z-WR	Zone Write	FUN114 Z-WR
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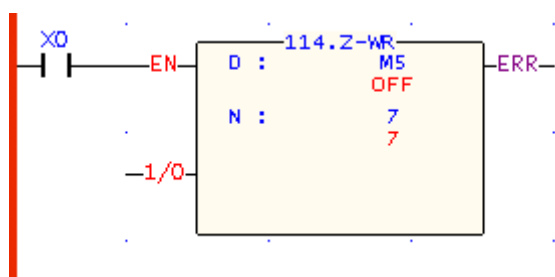
D : Starting address of being set or reset
 N : Quantity of being set or reset, 1~511
 D、N operand can combine V、Z、P0~P9 for index addressing while word operation

Range	Y	M	S	WY	WM	WS	TMR	CTR	HR	IR	OR	SR	ROR	DR	K	XR
	Y0	M0	S0	WY0	WM0	WS0	T0	C0	R0	R3840	R3904	R3968	R5000	D0		V、Z P0~P9
Operand	Y255	M1911	S99	WY240	WM1896	WS984	T255	C255	R3839	R3903	R3967	R4167	R8071	D4095		
D	○	○	○	○	○	○	○	○	○	○	○	○	○	○		○
N									○				○	○	1-511	○

- When operation control "EN"=1 or "EN ↑" (P instruction) changes from 0→1, it will perform the write operation according to the input status of write selection, the specified area of registers or bits will all be reset to 0 ("1/0"=0) or set to 1("1/0"=1).



- Above example, registers R0~R9 will be reset to 0 while X0=1.



- Above example, bits M5~M11 will be reset to 0 while X0=1.